

REMARKS

Applicant is in receipt of the Office Action mailed September 10, 2004. Claims 1-44 remain pending in the case. Reconsideration of the present case is earnestly requested in light of the following remarks.

Allowed Subject Matter

Applicant appreciates the allowed subject matter of claims 1-43.

Section 102 Rejections

Claim 44 and was rejected under 35 U.S.C. 102(b) as being clearly anticipated by Morgan (U.S. Patent 4,635,096, henceforth "Morgan"). Applicant respectfully disagrees.

Claim 44 recites:

44. A system configured to generate one or more waveform output streams, the system comprising:
a memory configured to store a plurality of waveform segments, wherein each waveform segment comprises one or more waveform samples;
a plurality of queues, wherein each queue is coupled to receive waveform segments from the memory, wherein each queue is configured to store a series of waveform segments for a respective one of the waveform output streams; and
one or more output interfaces, wherein each output interface is coupled to read waveform segments from at least one of the waveform segment queues that stores waveform segments for a respective one of the waveform data streams.

As the Examiner is certainly aware, anticipation requires the presence in a single prior art reference disclosure of each and every element of the claimed invention, arranged as in the claim. *Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick Co.*, 221 USPQ 481, 485 (Fed. Cir. 1984). The identical invention must be

shown in as complete detail as is contained in the claims. *Richardson v. Suzuki Motor Co.*, 9 USPQ2d 1913, 1920 (Fed. Cir. 1989).

The Office Action asserts that Morgan teaches all of the features and limitations of claim 44. Applicant respectfully submits that there are numerous features and limitations of claim 44 not taught or suggested by Morgan.

For example, the Office Action asserts equivalence between Morgan's RAMS 26-32 and Applicant's plurality of queues. However, Applicant notes that the only queues mentioned in Morgan are: a single queue for storing a switch sequence (described with reference to Figure 11, col. 18, lines 30-37) associated with sequence switch 314; and a single queue stored in auxiliary memory 51 that stores test patterns (described with reference to Figure 12, col. 19, lines 17-37; col. 20, lines 48-col. 21, line 30). Thus, in Morgan's system only one queue is used for storing patterns, and this one queue is described as stored in auxiliary memory 51, *not* in RAMS 26-32. Morgan's RAMS 26-32 are described in col. 3, line 3-col. 4, line 23, and elsewhere, and, although they are used to store "pattern data representative of portions of the test signal to be generated", nowhere are they described as comprising, storing, or implementing, a plurality of queues. Thus, Applicant respectfully submits that Morgan fails to teach or suggest "a plurality of queues, wherein each queue is coupled to receive waveform segments from the memory, wherein each queue is configured to store a series of waveform segments for a respective one of the waveform output streams", as presented in claim 44.

The Office Action further asserts equivalence between Morgan's bounce switch 324, multiplexer 34, and RAM OUT, and Applicant's "one or more output interfaces". However, Applicant submits that none of the bounce switch 324, multiplexer 34, and RAM OUT includes the limitation "wherein each output interface is coupled to read waveform segments from at least one of the waveform segment queues that stores waveform segments for a respective one of the waveform data streams". For example, bounce switch 34 is a switch. Applicant submits that the bounce switch is not operable to *read waveform segments from at least one of the waveform segment queues*. In fact, as described in col. 21, lines 14-21, which reads:

"...the CPU, in step 224, will determine whether the switch pushed is the bounce switch 324. If such is the case,

a cross-hatch pattern will be loaded *into the queue*”
(*emphasis added*),

the bounce switch initiates transfer of a pattern *into the single queue* used to store test patterns. Thus, Morgan’s bounce switch 324 is *not* equivalent to Applicant’s “one or more output interfaces”.

Similarly, Morgan’s multiplexer 34 receives input from RAMS 26-32, which have been shown above to *not* comprise a plurality of queues. Thus, Applicant submits that Morgan’s multiplexer 34 is *not* equivalent to Applicant’s “one or more output interfaces”.

Moreover, Applicant notes that Morgan’s RAM OUTs belong to RAMS 26-32, and so are specifically not operable to *read waveform segments from at least one of the waveform segment queues*, since, as argued above, the RAMS do not comprise a plurality of queues.

Thus, Applicant respectfully submits that Morgan fails to teach or suggest “one or more output interfaces, wherein each output interface is coupled to read waveform segments from at least one of the waveform segment queues that stores waveform segments for a respective one of the waveform data streams”, as presented in claim 44.

Thus, for at least the reasons provided above, Applicant submits that Morgan fails to teach all of the features and limitations of claim 44, and so claim 44 is patentably distinct and non-obvious over Morgan, and is thus allowable.

Removal of the 102 rejection of claim 44 is earnestly requested.

Applicant also asserts that numerous ones of the dependent claims recite further distinctions over the cited art. However, since the independent claims have been shown to be patentably distinct, a further discussion of the dependent claims is not necessary at this time.

CONCLUSION

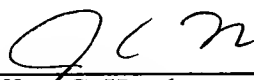
Applicant submits the application is in condition for allowance, and an early notice to that effect is requested.

If any extensions of time (under 37 C.F.R. § 1.136) are necessary to prevent the above referenced application(s) from becoming abandoned, Applicant(s) hereby petition for such extensions. If any fees are due, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert & Goetzel PC Deposit Account No. 50-1505/5150-56500/JCH.

Also enclosed herewith are the following items:

- ☒ Return Receipt Postcard
- ☒ ~~Notice of Change of Address~~ JCH
- ☐ Check in the amount of \$ for fees ().
- ☐ Other:

Respectfully submitted,



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